



Global LCD Panel Exchange Center

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Product Specification

MODEL: HM236WU3-100

BEIJING BOE Display TECHNOLOGY

SPEC. NUMBER	PRODUCT GROUP	Rev.0	ISSUE DATE	PAGE
S	TFT-LCD		2011. 12. 26	1 OF 27



京东方	PRODUCT GROUP	REV	ISSUE DATE
BOE	TFT- LCD PRODUCT	0	2011.12.26

REVISION HISTORY

EV. ECN	DESCRIPTION OF CHANGES	DATE	PREPARED
0	Initial Release	Dec. 26. 11'	Kim Woong
		1-1	
•			
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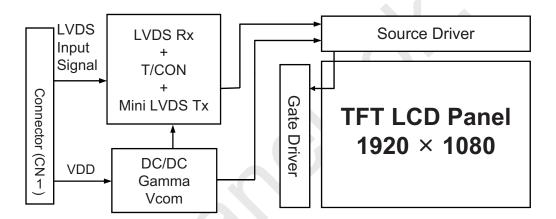


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1.0 GENERAL DESCRIPTION

1.1 Introduction

HM236WU3-100 is a color active matrix TFT LCD open cell using amorphous silicon TFT's (Thin Film Transistors) as an active switching devices. This open cell has a 23.6 inch diagonally measured active area with FHD resolutions (1920 horizontal by 1080 vertical pixel array). Each pixel is divided into RED, GREEN, BLUE dots which are arranged in vertical stripe and this module can display 16.7M colors. The TFT-LCD panel used for this module is adapted for a low reflection and higher color type.



1.2 Features

- LVDS Interface with 2 pixel / clock
- High-speed response
- 6-bit (Hi-FRC) color depth, display 16. 7M colors
- High luminance and contrast ratio, low reflection and wide viewing angle
- DE (Data Enable) only
- RoHS/Halogen Free
- TCO 5.0 compliant
- Gamma Correction

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1.3 Application

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- Desktop Type of PC & Workstation Use
- Slim-Size Display for Stand-alone Monitor
- Display Terminals for Control System
- Monitors for Process Controller

1.4 General Specification

The followings are general specifications at the model HM236WU3-100.

<Table 1. General Specifications>

Parameter	Specification	Unit	Remarks
Active area	521.28(H) × 293.22(V)	mm	
Number of pixels	1920(H) ×1080(V)	pixels	
Pixel pitch	0.2715(H) ×0.2715(V)	mm	
Pixel arrangement	RGB Vertical stripe		
Display colors	16.7M	colors	
Display mode	Normally White		
Weight	730 (Max.)	g	
Surface Treatment	Haze 25%, 3H		

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2.0 ABSOLUTE MAXIMUM RATINGS

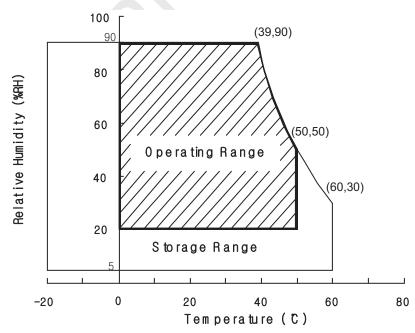
The followings are maximum values which, if exceed, may cause faulty operation or damage to the unit. The operational and non-operational maximum voltage and current values are listed in Table 2.

< Table 2. Absolute Maximum Ratings>

[VSS=GND=0V]

Parameter	Symbol	Min.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-0.3	6.0	V	Ta = 25 ℃
Logic Supply Voltage	V _{IN}	VSS-0.3	V _{DD} +0.3	♦ V	1a = 25 C
Operating Temperature	T _{OP}	0	+50	$^{\circ}$	1)
Storage Temperature	T _{ST}	-20	+60	$^{\circ}$	1)

Note: 1) Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C max. and no condensation of water.



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3.0 ELECTRICAL SPECIFICATIONS

3.1 Electrical Specifications

< Table 3. Electrical specifications >

[Ta =25±2 ℃]

Parameter		Min.	Тур.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	4.5	5.0	5.5	V	Note1
Power Supply Current	I _{DD}	-	900	1100	mA	Note i
In-Rush Current	I _{RUSH}	-	2.0	3.0	Α	Note 2
Permissible Input Ripple Voltage	V_{RF}	-	-	300	mV	$V_{DD} = 5.0V$
High Level Differential Input Threshold Voltage	V _{IH}	-		+100	mV	
Low Level Differential Input Threshold Voltage	V _{IL}	-100		-	mV	
Differential input voltage	V _{ID}	200	-	600	mV	
Differential input common mode voltage	Vcm	1.0	1.2	1.5		V _{IH} =100mV, V _{IL} =-100mV
Power Consumption	P _D	-	4.5	5.5	W	

Notes 1. The supply voltage is measured and specified at the interface connector of LCM. The current draw and power consumption specified is for VDD=5.0V, Frame rate=75Hz,Clock frequency = 92.9 MHz.

Test Pattern of power supply current

a) Typ: Color Test

b) Max : Skip Subpixel255



- 2. Duration of rush current is about 2 ms and rising time of VDD is 520 $\mu s \pm 20 \ \%$
- 3. Calculated value for reference (Input pins*VPIN ×IPIN) excluding inverter loss.

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4.0 OPTICAL SPECIFICATION

4.1 Overview

The test of Optical specifications shall be measured in a dark room (ambient luminance ≤ 1 lux and temperature = 25±2°C) with the equipment of Luminance meter system (Goniometer system and TOPCONE BM-5) and test unit shall be located at an approximate distance 50cm from the LCD surface at a viewing angle of θ and Φ equal to 0° . We refer to $\theta_{\varnothing=0}$ (= θ_3) as the 3 o'clock direction (the "right"), $\theta_{\varnothing=90}$ (= θ_{12}) as the 12 o'clock direction ("upward"), $\theta_{\varnothing=180}$ (= θ_{9}) as the 9 o'clock direction ("left") and $\theta_{\varnothing=270}$ (= θ_6) as the 6 o'clock direction ("bottom"). While scanning θ and/or \emptyset , the center of the measuring spot on the Display surface shall stay fixed. The measurement shall be executed after 30 minutes warm-up period. VDD shall be 5.0V +/-10% at 25°C. Optimum viewing angle direction is 6 'clock.

4.2 Optical Specifications

[VDD = 5.0V, Frame rate = 60Hz, Clock = 74.25MHz, I_{BL} = 400mA, Ta =25 \pm 2 °C]								
Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	Horizontal	Θ_3		75	85	-	Deg.	
Viewing	попиона	Θ_9	Θ ₉	75	85	1	Deg.	Note 2
Angle	Vertical	Θ ₁₂	CR > 10	70	80	-	Deg.	Note 2
	vertical	Θ_6		70	80	-	Deg.	
Luminance Con	trast ratio	CR		700	1000			Note 3
Cell Transmitta	nce	Tr			5.3		%	Note 4
Luminance of V	Vhite	Y_{w}		200	250		cd/m ²	Note 5
White luminance	e uniformity	ΔΥ		75	80		%	Note 6
	White Red	W_x	⊝ = 0°	0.283	0.313	0.343	-	
		W_y	(Center)	0.299	0.329	0.359	-	
		R_x	Normal Viewing	0.613	0.643	0.673	-	
Reproduction		R_y	Angle	0.312	0.342	0.372	-	N 1 7
of color	0,,,,,,	G _x		0.287	0.317	0.347	-	Note 7
	Green	G _y		0.598	0.628	0.658	-	
	Dive	B _x		0.118	0.148	0.178	-	
	Blue	B _y		0.034	0.064	0.094	-	
Response	Rising	T _r			1.5	2.5	ms	Note 0
Time	Falling	T_f			3.5	5.5	ms	Note 8
Cross	Talk	CT		-	-	2.0	%	Note 9

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Note:

- 1. The value in upper table are based on BLU provided by BOEDT
- 2. Viewing angle is the angle at which the contrast ratio is greater than 10. The viewing are determined for the horizontal or 3, 9 o'clock direction and the vertical or 6, 12 o'clock direction with respect to the optical axis which is normal to the LCD surface.
- 3. Contrast measurements shall be made at viewing angle of = 0 and at the center of the LCD surface. Luminance shall be measured with all pixels in the view field set first to white, then to the dark (black) state. (See FIGURE 1 shown in Appendix) Luminance Contrast Ratio (CR) is defined mathematically.

CR = Luminance when displaying a white raster
Luminance when displaying a black raster

4. Luminance of LCD module shall be made without signal input. Cell transmittance is defined mathematically, BLU provided by BOEDT.

- 5. Center Luminance of white is defined as the LCD surface. Luminance shall be measured with all pixels in the view field set first to white. This measurement shall be taken at the locations shown in FIGURE 2 for a total of the measurements per display.
- 6. The White luminance uniformity on LCD surface is then expressed as : $\Delta Y = ($ Minimum Luminance of 9points / Maximum Luminance of 9points) * 100 (See FIGURE 2 shown in Appendix).
- 7. The color chromaticity coordinates specified in above Table shall be calculated from the spectral data measured with all pixels first in red, green, blue and white.

 Measurements shall be made at the center of the panel with BLU provided by BOEDT.
- 8. The electro-optical response time measurements shall be made as FIGURE 3 shown in Appendix by switching the "data" input signal ON and OFF. The times needed for the luminance to change from 10% to 90% is Td, and 90% to 10% is Tr.
- 9. Cross-Talk of one area of the LCD surface by another shall be measured by comparing the luminance (YA) of a 25mm diameter area, with all display pixels set to a gray level, to the luminance (YB) of that same area when any adjacent area is driven dark. (See FIGURE 4 shown in Appendix).

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5.0 INTERFACE CONNECTION.

5.1 Electrical Interface Connection

• CN11 Module Side Connector : UJU IS100-L30R-C23or Equivalent User Side Connector: JAE FI-X30H or Equivalent

Pin No	Symbol	Function	Remark
1	RXO0-	Negative Transmission data of Pixel 0 (ODD)	
2	RXO0+	Positive Transmission data of Pixel 0 (ODD)	
3	RXO1-	Negative Transmission data of Pixel 1 (ODD)	
4	RXO1+	Positive Transmission data of Pixel 1 (ODD)	
5	RXO2-	Negative Transmission data of Pixel 2 (ODD)	
6	RXO2+	Positive Transmission data of Pixel 2 (ODD)	
7	GND	Power Ground	
8	RXOC-	Negative Transmission Clock (ODD)	
9	RXOC+	Positive Transmission Clock (ODD)	
10	RXO3-	Negative Transmission data of Pixel 3 (ODD)	
11	RXO3+	Positive Transmission data of Pixel 3 (ODD)	
12	RXE0-	Negative Transmission data of Pixel 0 (EVEN)	
13	RXE0+	Positive Transmission data of Pixel 0 (EVEN)	
14	GND	Power Ground	
15	RXE1-	Negative Transmission data of Pixel 1 (EVEN)	
16	RXE1+	Positive Transmission data of Pixel 1 (EVEN)	
17	GNG	Power Ground	
18	RXE2-	Negative Transmission data of Pixel 2 (EVEN)	
19	RXE2+	Positive Transmission data of Pixel 2 (EVEN)	
20	RXEC-	Negative Transmission Clock (EVEN)	
21	RXEC+	Positive Transmission Clock (EVEN)	
22	RXE3-	Negative Transmission data of Pixel 3 (EVEN)	
23	RXE3+	Positive Transmission data of Pixel 3 (EVEN)	
24	GND	Power Ground	Note 1
25	NC		
26	NC	No. Connection	
27	NC		
28	VDD		
29	VDD	Power Supply: +5V	
30	VDD		

Note 1: This pin should be connected with GND.

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5.2 LVDS Interface (Tx; THC63LVDF83A or Equivalent)

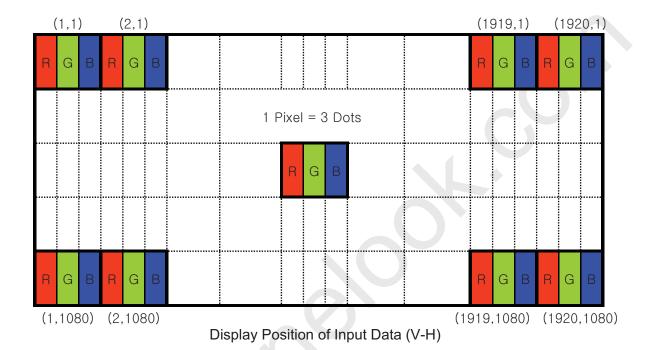
	Input	Trans	mitter	Inter	face	HM236WU3-100 (CN11)	Remark					
	Signal	Pin No.	Pin No.	System (Tx)	TFT-LCD (Rx)	Pin No.						
	OR0	51										
	OR1	52										
	OR2	54	48	OUT0-	RXO0-	1						
	OR3	55	46 47	OUT0+	RX00+	1 2						
	OR4	56	.,	0010	10.00							
	OR5	3										
	OG0	4										
	OG1	6				•						
	OG2	7										
	OG3	11		40]	DVO4						
	OG4	12		OUT1- OUT1+	RXO1- RXO1+	3 4						
	OG5	14			0011	10.01	7					
	OB0	15										
	OB1	19										
L V	OB2	20										
Ď	OB3	22										
S	OB4	23				OLUTO	DVOO	-				
	OB5	24					RXO2- RXO2+	5 6				
	Hsync	27			0012	10.02						
	Vsync	28										
	DE	30										
	MCLK	31	40	CLK OUT-	RXO CLK-	8						
			39	CLK OUT+	RXO CLK+	9						
	OR6	50										
	OR7	2										
	OG6	8	38	OUT3-	RXO3-	10						
	OG7	10	37	OUT3+	RXO3+	11						
	OB6	16	-									
	OB7	18										
	RSVD	25										

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5.3 Data Input Format



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6.0 SIGNAL TIMING SPECIFICATION

6.1 The HM236WU3-100 is operated by the DE only.

	Item	Symbols	Min	Тур	Max	Unit
	Frequency	1/Tc	58.54	74.25	98	MHz
Clock	High Time	Tch	-	4/7Tc	<u> </u>	
	Low Time	Tcl	-	4/7Tc	ı	
	Frame Period		1115	1126	1136	lines
Fra			50	60	75	Hz
			20	16.7	13.3	ms
Vertica	Vertical Display Period		1	1080	-	lines
One line	One line Scanning Period		1050	1100	1150	clocks
Horizont	al Display Period	Thd	960	960	960	clocks

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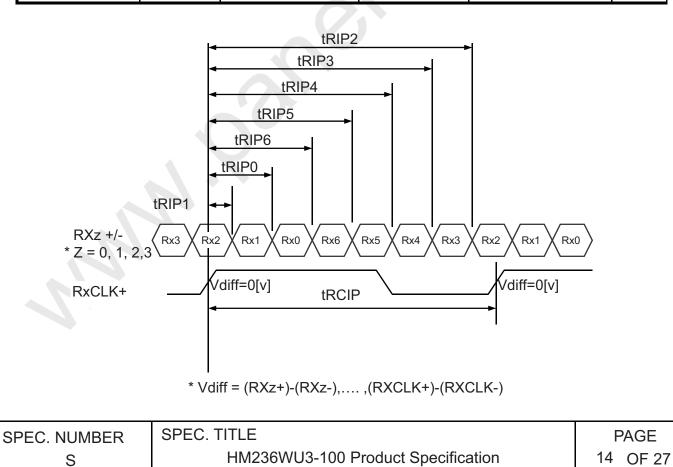
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6.2 LVDS Rx Interface Timing Parameter

The specification of the LVDS Rx interface timing parameter is shown in Table 4.

<Table 4. LVDS Rx Interface Timing Specification>

Item	Symbol	Min	Тур	Max	Unit
CLKIN Period	tRCIP	10.20	13.47	17.08	nsec
Input Data 0	tRIP1	-0.4	0.0	+0.4	nsec
Input Data 1	tRIP0	tRCIP/7-0.4	tRCIP/7	tRCIP/7+0.4	nsec
Input Data 2	tRIP6	2 ×tRCIP/7-0.4	2 ×tRCIP/7	2 ×tRCIP/7+0.4	nsec
Input Data 3	tRIP5	3 ×tRCIP/7-0.4	3 ×tRCIP/7	3 ×tRCIP/7+0.4	nsec
Input Data 4	tRIP4	4 ×tRCIP/7-0.4	4 ×tRCIP/7	4 ×tRCIP/7+0.4	nsec
Input Data 5	tRIP3	5 ×tRCIP/7-0.4	5 ×tRCIP/7	5 ×tRCIP/7+0.4	nsec
Input Data 6	tRIP2	6 ×tRCIP/7-0.4	6 ×tRCIP/7	6 ×tRCIP/7+0.4	nsec

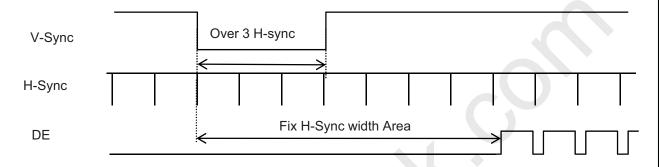






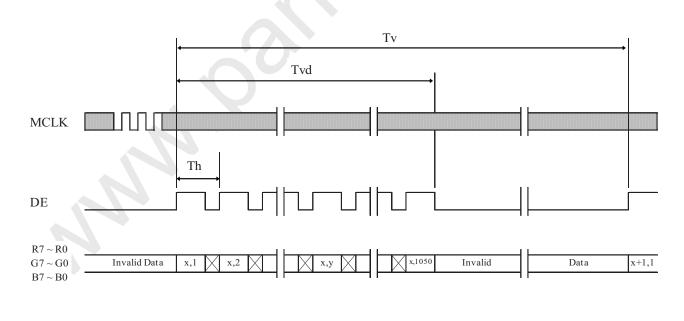
7.0 SIGNAL TIMING WAVEFORMS OF INTERFACE SIGNAL

7.1 Sync Timing Waveforms



- 1) Need over 3 H-sync during V-Sync Low
- 2) Fix H-Sync width from V-Sync falling edge to first rising edge

7.2 Vertical Timing Waveforms

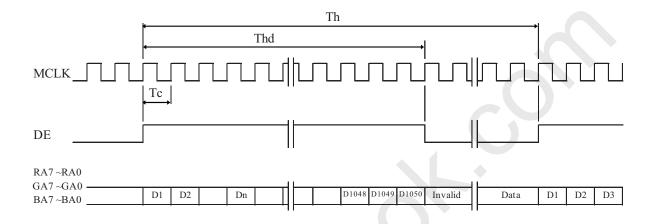


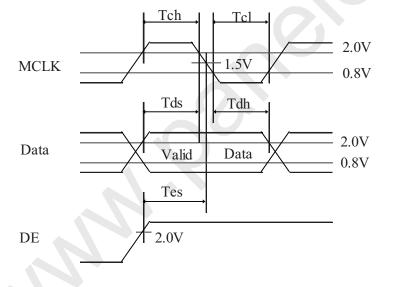
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7.3 Horizontal Timing Waveforms





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8.0 INPUT SIGNALS, BASIC DISPLAY COLORS & GRAY SCALE OF COLORS

0.10.0	0 1 .			RI	ΞD	DA	ГΑ				(GRI	EEN	۱D	AT/	4				BL	UE	DΑ	TA		
Color & G	ray Scale	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	B6	B5	B4	В3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
•	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Dania Calara	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Basic Colors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale	\triangle				,	1							,									↑			
of RED	∇					ļ							L,									\downarrow			
ĺ	Brighter	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ĺ	∇	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
of GREEN	\triangle				,	1							,	1								↑			
OI GREEN	∇				,								,					1							
	Brighter	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	∇	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	\triangle	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
of BLUE	\triangle				· ′	^							,	1								↑			
OLDEOE	∇				,	\downarrow							,	ļ								\downarrow			
	Brighter	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	∇	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1
Gray Scale	Darker	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	1	0
of WHITE	\triangle					<u> </u>							_									<u> </u>			
OT WHITE	∇				,	ļ							,	ļ								\downarrow			
	Brighter	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1
[∇	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1

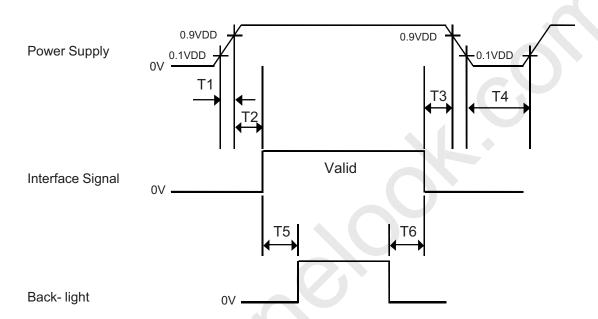
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9.0 POWER SEQUENCE

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence shall be as shown in below



- 0.5 ms ≤ T1 ≤ 10 ms
- \bullet 0 \leq T2 \leq 50 ms
- \bullet 0 \leq T3 \leq 50 ms
- \bullet 1 sec \leq T4
- \bullet 200 ms \leq T5
- 200 ms ≤ T6

Notes:

- 1. When the power supply VDD is 0V, keep the level of input signals on the low or keep high impedance.
- 2. Do not keep the interface signal high impedance when power is on.
- 3. Back Light must be turn on after power for logic and interface signal are valid.

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10.0 MECHANICAL CHARACTERISTICS

10.1 Dimensional Requirements

FIGURE 6 (located in Appendix) shows mechanical outlines for the model HM236WU3-100. Other parameters are shown in Table 5.

<Table 5. Dimensional Parameters>

Parameter	Specification	Unit
Dimensional outline	544.8(H) × 320.5(V) × 9.6(D) typ	mm
Weight	2200(typ)	gram
Active area	521.28 (H) × 293.22 (V)	mm
Pixel pitch	0.2715 (H) ×0.2715 (V)	mm
Number of pixels	1920 (H)×1080 (V) (1 pixel = R + G + B dots)	pixels

10.2 Anti-Glare and Polarizer Hardness.

The surface of the LCD has an anti-glare coating to minimize reflection and a coating to reduce scratching.

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11.0 RELIABLITY TEST

The Reliability test items and its conditions are shown in below.

<Table 6. Reliability Test Parameters >

	<u> </u>			
No	Test Items	Conditions		
1	High temperature storage test	Ta = 60 ℃, 240 h	rs	
2	Low temperature storage test	Ta = -20 ℃, 240 h	nrs	
3	High temperature & high humidity operation test	ty Ta = 50 ℃, 80%RH, 240hrs		
4	High temperature operation test	Ta = 50 ℃, 240hr	S	
5	Low temperature operation test	Ta = 0°C, 240hrs	3	
6	Thermal shock	Ta = -20 $^{\circ}$ C \leftrightarrow 60 $^{\circ}$ C (0.5 hr), 100 cycle		
7	Vibration test (non-operating)	Frequency	Random,10 ~ 300 Hz, 30 min/Axis	
7		Gravity / AMP	1.5 Grms	
		Period	X, Y, Z 30 min	
	Shock test (non-operating)	Gravity	50G	
8		Pulse width	11msec, sine wave	
	(9)	Direction	\pm X, \pm Y, \pm Z Once for each	
9 Electro-static discharge test (non-operating) Air : 150 pF, 330Ω, 15 k Contact : 150 pF, 330Ω, 8 K				

Notes:

- 1. The test are done with LCD modules (Use BOE BLU)
- 2. The test is done with a package (20Pcs open cell/ 1 Box)shown in section 14.

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12.0 HANDLING & CAUTIONS

- (1) Cautions when taking out the module
 - Pick the pouch only, when taking out module from a shipping package.
- (2) Cautions for handling the module
 - As the electrostatic discharges may break the LCD module, handle the LCD module with care. Peel a protection sheet off from the LCD panel surface as slowly as possible.
 - As the LCD panel and back light element are made from fragile glass material, impulse and pressure to the LCD module should be avoided.
 - As the surface of the polarizer is very soft and easily scratched, use a soft dry cloth without chemicals for cleaning.
 - Do not pull the interface connector in or out while the LCD module is operating.
 - Put the module display side down on a flat horizontal plane.
 - Handle connectors and cables with care.
- (3) Cautions for the operation
 - When the module is operating, do not lose CLK, ENAB signals. If any one of these signals is lost, the LCD panel would be damaged.
 - Obey the supply voltage sequence. If wrong sequence is applied, the module would be damaged.
- (4) Cautions for the atmosphere
 - Dew drop atmosphere should be avoided.
 - Do not store and/or operate the LCD module in a high temperature and/or humidity atmosphere. Storage in an electro-conductive polymer packing pouch and under relatively low temperature atmosphere is recommended.
- (5) Cautions for the module characteristics
 - Do not apply fixed pattern data signal to the LCD module at product aging.
 - Applying fixed pattern for a long time may cause image sticking.
- (6) Other cautions
 - Do not disassemble and/or re-assemble LCD module.
 - Do not re-adjust variable resistor or switch etc.
 - •When returning the module for repair or etc., Please pack the module not to be broken. We recommend to use the original shipping packages.

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13.0 PRODUCT SERIAL NUMBER



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- 1. Control Number
- 2. Rank / Grade
- 3. Line Classification
- 4. Year (2001:01, 2002:02, ...)

- 5. Month (1,2,3, ..., 9, X, Y, Z)
- 6. Internal Use
- 7. Serial Number

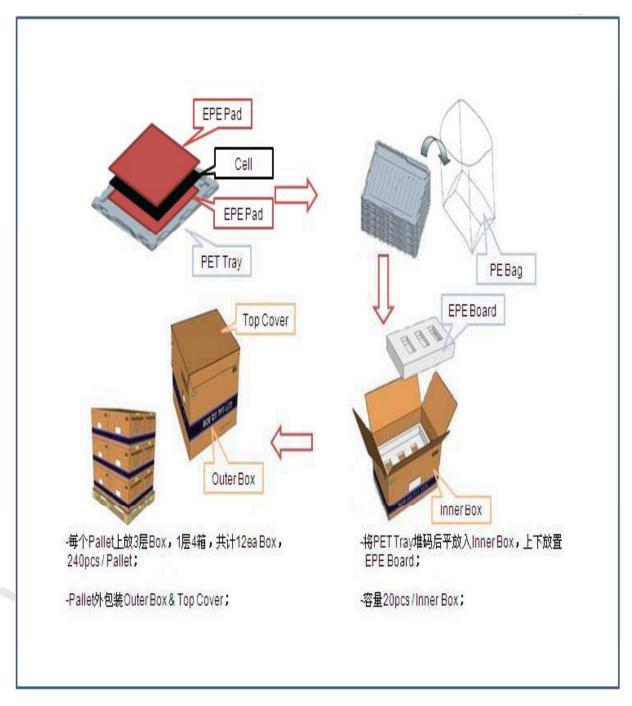
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14.0 Packing

14.1 Packing Order



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14.2 Packing Note

Box Dimension : 505mm*695mm*279Package Quantity in one Box : 20 pcs

14.3 Box label

• Label Size :110mm*55mm

Contents

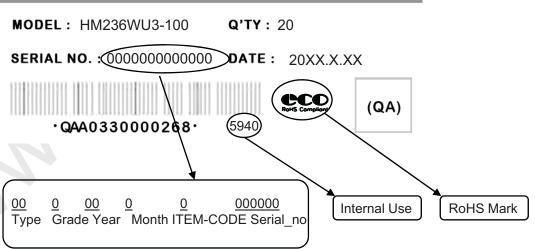
Model: HM236WU3-100

Q'ty: Open cell 20 Q'ty in one box

Serial No.: Box Serial No. See next page for detail description.

Date: Packing Date





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15.0 APPENDIX

Figure 1. Measurement Set Up

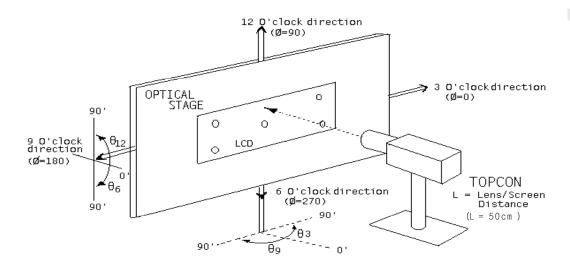
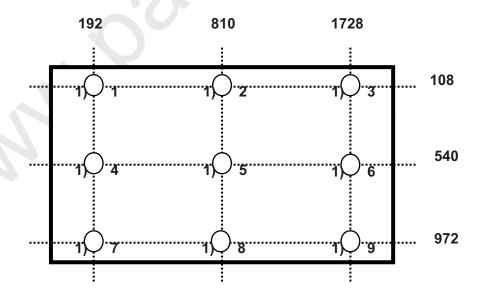


Figure 2. White Luminance and Uniformity Measurement Locations (9 points)



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Figure 3. Response Time Testing

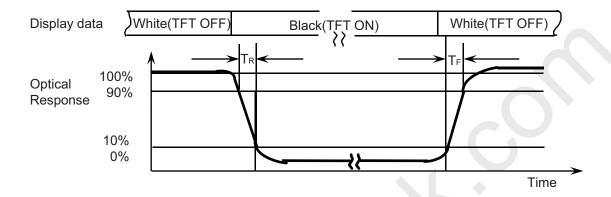
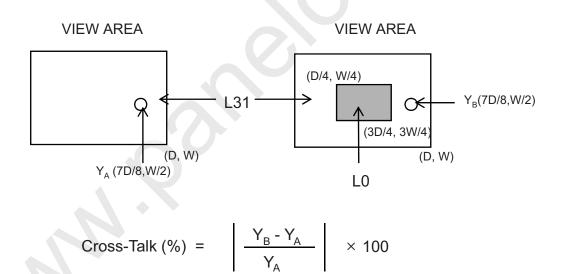


Figure 4. Cross Modulation Test Description



Where: Y_A = Initial luminance of measured area (cd/m²) Y_B = Subsequent luminance of measured area (cd/m²)

The location measured will be exactly the same in both patterns

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